



(RESEARCH ARTICLE)



Performance analysis of H6 topologies for transformerless inverter photovoltaic grid tied system

Md. Aftab Alam *

Blackboard E-Learning Pvt. Ltd, Electronics and Communication Engineering, Noida, UP, India.

World Journal of Advanced Engineering Technology and Sciences, 2026, 18(03), 169-176

Publication history: Received on 21 January 2026; revised on 05 March 2026; accepted on 07 March 2026

Article DOI: <https://doi.org/10.30574/wjaets.2026.18.3.0142>

Abstract

One of the most important and common parts of the modern power systems is the grid-connected photovoltaic (PV) systems. Recently, these systems have gotten a big revolution due to the introduction of the transformerless inverters. The introduction of transformerless inverters has indeed brought about advantages such as small size, low cost, and high efficiency, but it has also raised safety concerns, particularly with leakage current. Various researches were directed toward evolving their performance and diminishing the leakage current to the standard limits. This paper presents an H6 inverter topology that reduces leakage current, conduction losses and improves inverter efficiency. In order to verify the theoretical explanations, the proposed topologies are simulated in MATLAB/Simulink. Simulations have been conducted to compare the performance of H6 topology with conventional topology.

Keywords: PV System; Leakage Current 2; Transformerless Inverter; H6 topology; Common mode voltage

1. Introduction

Solar photovoltaics provide a renewable source of energy that is essential to today's growing demand for energy worldwide. Free and clean solar energy is abundant and available everywhere. In comparison with other renewable energy sources, it is becoming more popular. PV technology has advanced significantly in recent years, resulting in lower production costs. PV systems convert the energy of the sun into DC power. Depending on whether or not a transformer is present in the grid-connected PV system, there are transformer-based PV systems and transformerless PV systems. The PV and grid are galvanically separated when a transformer is present and there is no current leakage and no direct current is supplied to the network. However, it is heavy, bulky and expensive to include a transformer in the circuit. As a result, PV systems are less efficient when a transformer is present. The result was grid-connected transformerless photovoltaic systems. Besides being light and small, they are also cheaper. They also provide better efficiency than grid-connected PV systems with transformers. However, its main disadvantage is that it cannot provide galvanic isolation. A large common current is present from the PV to the grid, which presents a safety concern.

Changes in PV potential and ground potential create stray capacitance (C_{pv}) in transformerless systems. The common voltage (V_{cm}) relates to this potential change. A number of factors can affect the stray capacitor, including moisture and dust, grounded PV cell frames and surfaces, and the distance between solar cells and modules. By charging and discharging the parasitic capacitance, common mode voltages generate leakage currents called common mode currents (I_{cm}). There are system losses, electromagnetic interference and network ripple due to leakage currents.

Since three-voltage levels are generated across the filter, unipolar modulation causes low losses. Comparing the bipolar and unipolar SPWM techniques, the unipolar SPWM technique provides higher efficiency. Due to the two voltage levels

* Corresponding author: Md.Aftab Alam

generated by bipolar modulation, filter inductor suffers from high core losses and switching losses. Compared to unipolar modulation, it has lower efficiency. Low losses and high efficiency make this technique ideal.

The H6 inverters are discussed in detail in this paper. The introduction of the H6 inverter concept overcomes the disadvantages of the H4 and H5 inverters, such as low efficiency and high conduction loss. When operating in active mode, in order to mitigate voltage fluctuations and leakage current, the H6 topology is designed with SPWM modulation strategies. There is a structure that ensures that only two switches are activated. The results of this study show that H6 inverters have lower conduction loss than H4. MATLAB/Simulink is used for all simulations in this Paper.

2. Methodology

2.1. H6 Inverter topology

In this paper, a transformerless H6 converter without leakage current has been designed, which reduces losses while increasing efficiency. Figure 1 shows the circuit for this inverter. Solar arrays become disconnected from the grid during freewheeling. Conduction loss has been comparatively reduced and efficiency has also improved, despite the increased number of switches. Keeping the common mode voltage constant helps eliminate leakage current.

A unipolar sinusoidal PWM modulation technique is used. V_{pv} stands for photovoltaic panel. Filtration is performed using an LCL filter. Electrical grids are represented by V_{grid} . The input DC link capacity is represented by C_{pv} . Since H-bridge inverters use transformers that do not have leakage current problems, typical inverter designs may not maintain a constant common-mode voltage. In the absence of transformers, the PV and grid are galvanically coupled, leakage currents are resulting from this. Maintaining constant common mode voltages can reduce leakage currents in such a situation.

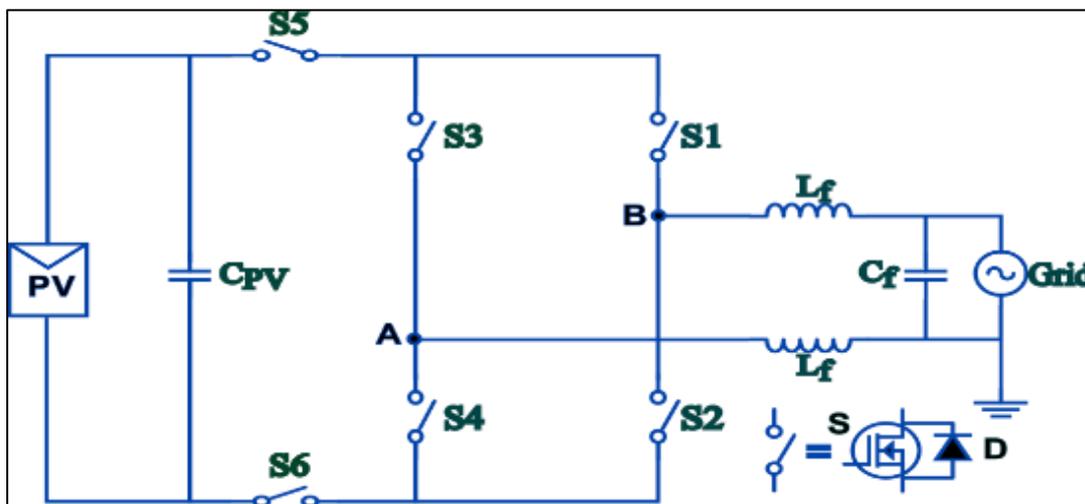


Figure 1 H6 inverter topology

2.2. Mode of operation of H6 Inverter topology

A total of four modes are available, including two active modes and two freewheeling modes.

2.2.1. Mode-1 Active mode

In active mode, $S1$ and $S6$ are turned on; $S4$ and $S5$ are switched to high frequency during the positive half cycle.

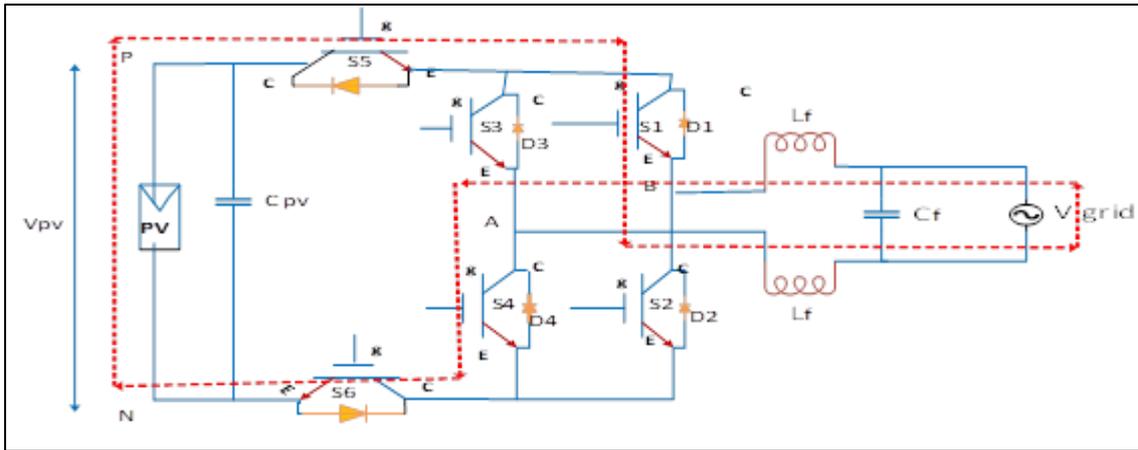


Figure 2 First Mode of operation of H6 topology

2.2.2. Mode-2 Freewheeling mode

Current flows through switch S1 in freewheeling mode during positive half and anti-parallel diode D3. The other switches are also all off.

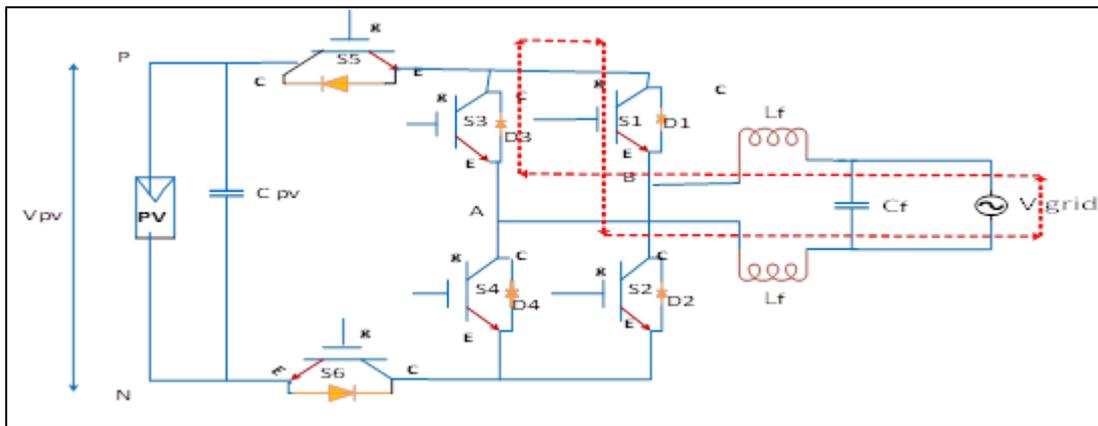


Figure 3 Second Mode of operation of H6 topology

2.2.3. Mode-3 Active mode

During active mode, S2 and S5 are ON; S3 and S6 switch at high frequency in negative half cycles, while S1 and S4 will remain off during negative half cycle active mode.

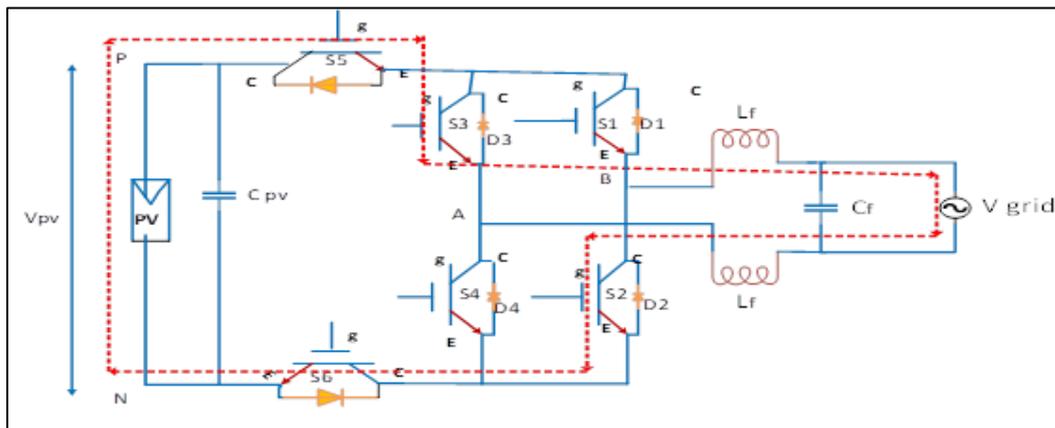


Figure 4 Third Mode of operation of H6 topology

2.2.4. Mode-4 Freewheeling mode

In the negative half-cycle freewheeling mode, current flows through D1 and S3.

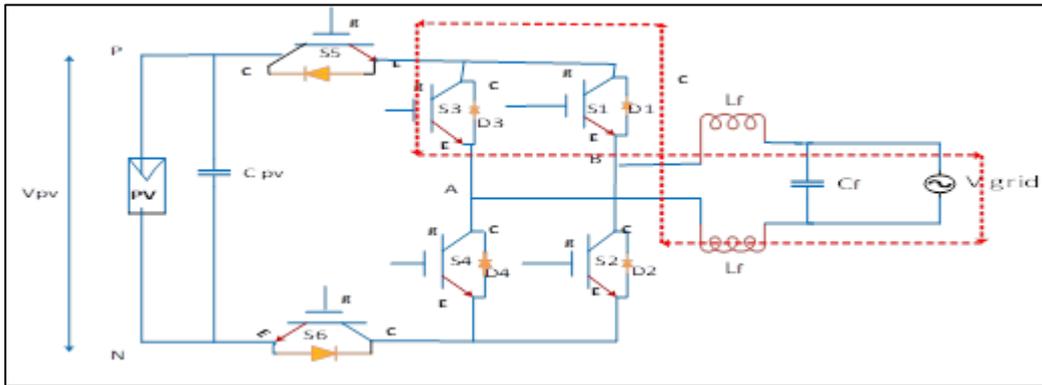


Figure 5 Fourth Mode of operation of H6 topology

3. Simulation result and discussion

MATLAB/Simulink is used for H6 inverters. Simulations are used to analyze inverter performance. Fig. 6. Simulation model for H6 inverter topology with unipolar modulation. Simulation has conducted using the parameters recorded in Table 1.

Table 1 Simulation parameters

DCinput_voltage	400Vdc
Vg	1 Ph 230 V (RMS), 50 Hz
Grid_frequency	50 HZ
Rated Power	2KW
Switching_Frequency	10KHZ
Dc-Link_capacitors	2200 μF
Inductors(Filter)	3 mH
Capacitor (Filter)	6 nF
Stray_capacitor	300 nF
Ground_Impedance	11Ω

According to Figure 7, the simulation of inverter H6 shows minimum leakage current and maintains constant common-mode-voltage. The leakage-current is reduced and the inverter is more efficient. Compared H6 inverter with conventional inverters, it achieves a lower leakage current and a more stable common mode voltage during operation. Matched to conventional topology, H6 inverters have relatively low conduction loss. H6 inverters therefore perform better than conventional topologies.

The table 2. below records the change in leakage current value from the minimum load of 200W to the full load possible for the given panel, i.e., 2kW.

Table 2 Variation of Leakage Current with load

Load (W)	Leakage Current (mA)_RMS
200	149.1
400	108.1
600	83.9
800	69.95
1000	61.41
1200	55.89
1400	52.14
1600	49.5
1800	46.13
2000	42.47

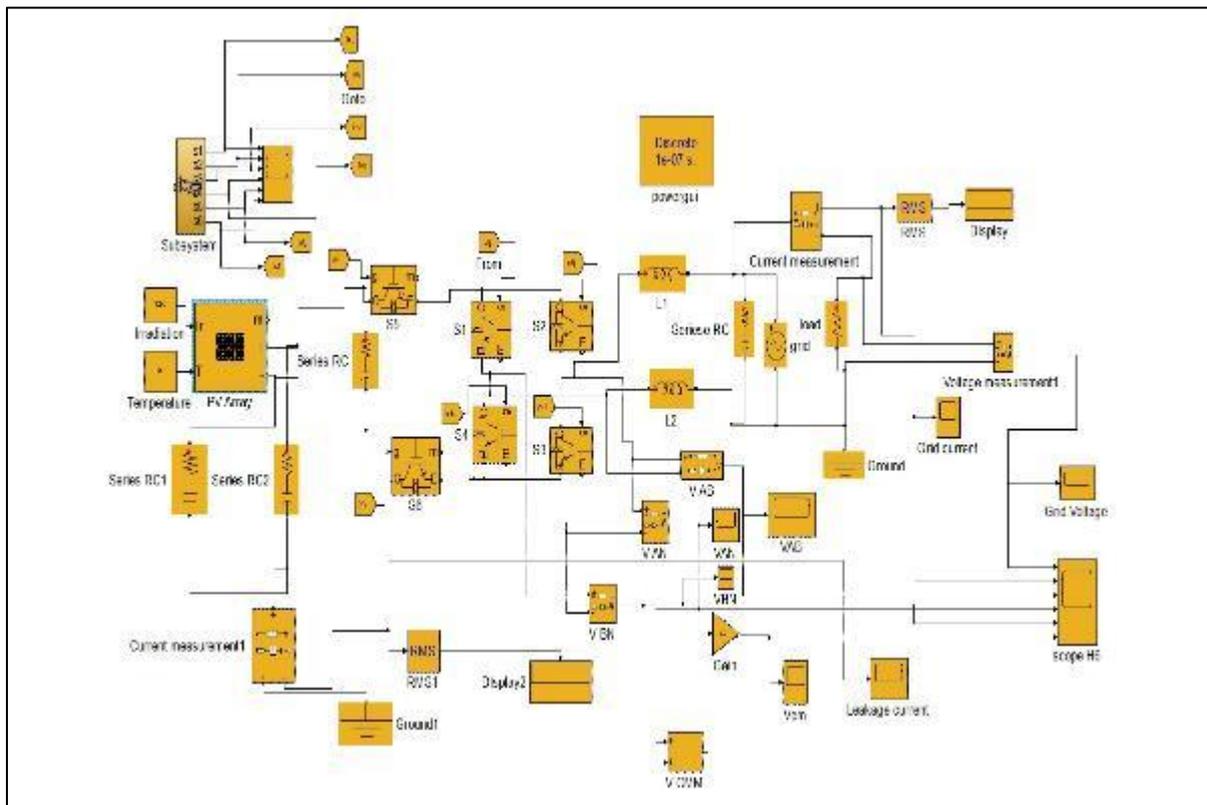


Figure 6 Simulation model for H6 inverter topology with unipolar modulation

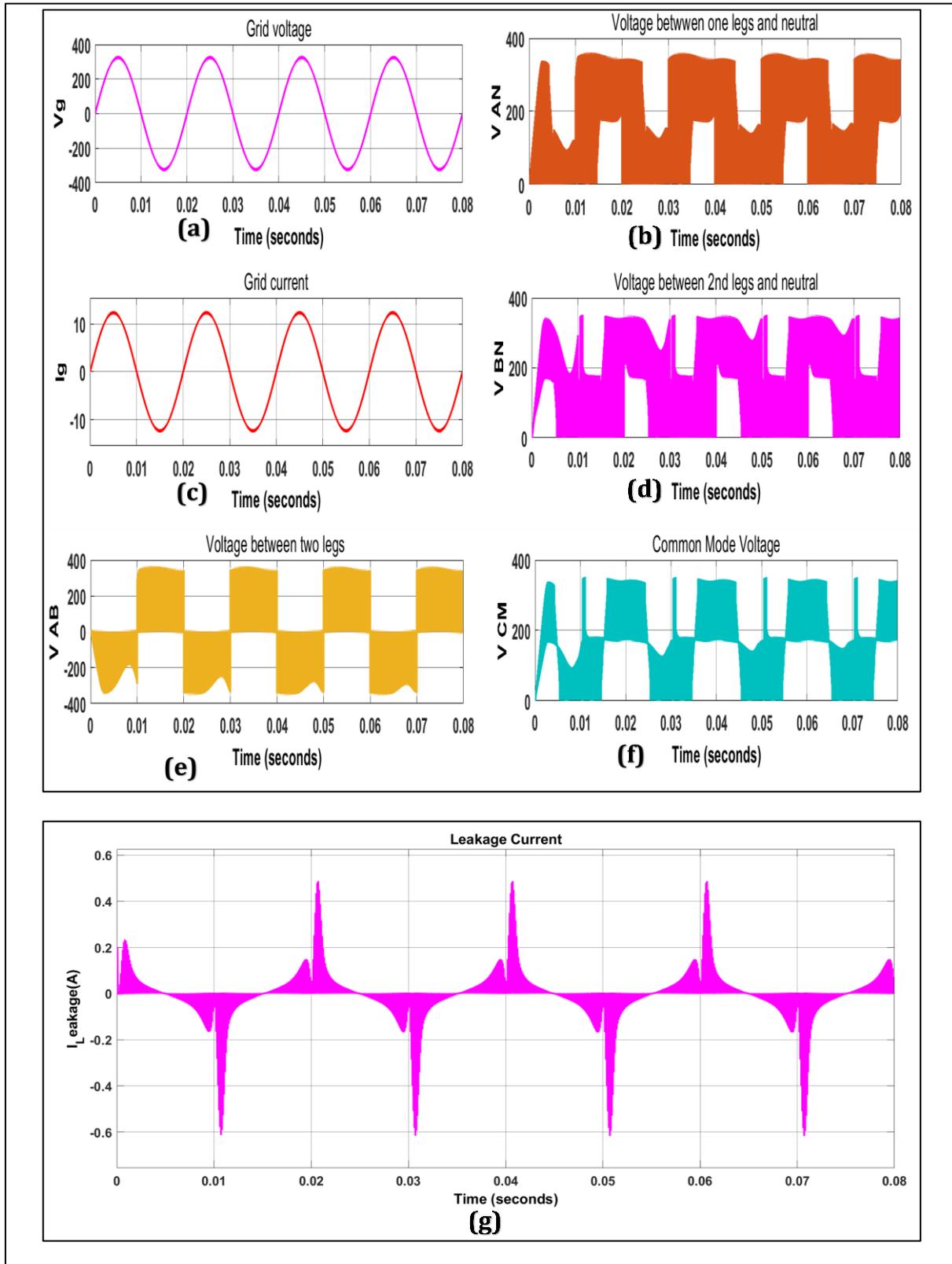


Figure 7 (a) V_g , (b) V_{AN} , (c) I_g , (d) V_{BN} , (e) V_{AB} , (f) V_{CM} , and (g) leakage current waveforms for H6 inverter topology with unipolar modulation

4. Conclusion

This paper presents an H6 inverter topology that reduces leakage current, conduction losses and improves inverter efficiency. A detailed analysis of H6 inverters is performed in order to minimize leakage currents in grid-connected transformerless inverter circuits. Simulation results show that even though there are six switches in the circuit, the H6 inverter performs much better in suppressing leakage current than conventional inverters. Therefore, the H6 inverter is a promising topology as it reduces losses, eliminates leakage current, and increases inverter efficiency. The H6 inverter shows better performance than the conventional inverter topologies.

References

- [1] Siddiqui, A., & Asim, M. (2025). Advancement, challenges and solutions of PV integrated battery energy storage systems: A review. *Journal of Energy Storage*, 132, 117996.
- [2] Apheratsakun, N., Bhaganagarapu, S. R., and Techakittiroj, K. (2005). Implementation of a Single-phase Unipolar Inverter Using DSP TMS320F241. *AUJT* 8(4): 191-195.
- [3] Ishtiaq, A., Siddiqui, M. A., Asim, M., & Laskar, S. H. (2023, December). Load Frequency Control for Single-Area System Using Modified PSO and Modified Grass Hopper Optimization-Based PID and PI Controllers. In *International Conference on Signal, Machines, Automation, and Algorithm* (pp. 687-695). Singapore: Springer Nature Singapore.
- [4] Aparnathi, R. R., and Dwivedi, V. V. (2014). Design and simulation low voltage single-phase transformerless photovoltaic inverter. *Telkomnika Indonesian Journal of Electrical Engineering* 12(7): 5163-5173.
- [5] Kumar Tiwari, V., Asim, M., Aftab Alam, M. D., Imran Siddiqui, M., & Alam, M. A. (2024). Implementation of Single-Phase Shift (SPS) and Extended Phase Shift (EPS) for Dual Active Bridge (DAB). *Indian Journal of Science and Technology*, 17(41), 4262-4269.
- [6] Albalawi, H., and Zaid, S. A. (2018). An H5 transformerless inverter for grid connected PV systems with improved utilization factor and a simple maximum power point algorithm. *Energies* 11(11): 2912.
- [7] Makwana, P., Asim, M., Alam, M. A., Siddiqui, A., & Bakhsh, F. I. (2024, November). Enhanced Working of Grid-linked PV with Incremental Conductance MPPT and Integrated Battery Storage. In *2024 Second International Conference Computational and Characterization Techniques in Engineering & Sciences (IC3TES)* (pp. 1-6). IEEE.
- [8] Cha, W. J., Kim, K. T., Cho, Y. W., Lee, S. H., and Kwon, B. H. (2015). Evaluation and analysis of transformerless photovoltaic inverter topology for efficiency improvement and reduction of leakage current. *IET Power Electronics* 8(2): 255-267.
- [9] Chamarthi, P., Rajeev, M., and Agarwal, V. (2015). A novel single stage zero leakage current transformer-less inverter for grid connected PV systems. In *2015 IEEE 42nd photovoltaic specialist conference (PVSC)*: 1-5.
- [10] Ministry of New & Renewable Energy, Government of India. Physical Progress. 2022.
- [11] Verma M. Analysis, Design, and Control of a Single-Phase Single-Stage Grid-Connected Transformerless Solar Inverter. Quebec, Canada. 2019. Available from.
- [12] Desai AA, Mikkili S, Senjyu T. Novel H6 Transformerless Inverter for Grid Connected Photovoltaic System to Reduce the Conduction Loss and Enhance Efficiency. *Energies*. 2022;15(10):3789.
- [13] Siddiqui MI, Khan I, Ali MA. Transformerless Inverter Topologies for Grid-Connected PV System.
- [14] Aljafari B, Loganathan AK, Vairavasundaram I, Ramachadran S, Nagarajan AP. Analysis of a Photovoltaic System Based on a Highly Efficient Single-Phase Transformerless Inverter. *Energies*. 2022;15(17):6145.
- [15] Janardhan G, Babu NNVS, Srinivas GN. Single phase transformerless inverter for grid connected photovoltaic system with reduced leakage current. *Electrical Engineering & Electromechanics*. 2022;(5):36-40. 11) Liu Y, Zhou Y, Guo B, Wang H, Su M, Liang X, et al. Bimodal Transformerless Inverter With Three Switches. *IEEE Transactions on Industrial Electronics*.
- [16] Barater, D., Buticchi, G., Lorenzani, E., and Concari, C. (2013). Active common-mode filter for ground leakage current reduction in grid-connected PV converters operating with arbitrary power factor. *IEEE Transactions on Industrial Electronics* 61(8): 3940-3950.

- [17] Chaniago, K., Selvaraj, J., and Rahim, N. A. (2008). Implementation of single-phase grid connected inverter using TMS320F2812. *In 2008 3rd IEEE Conference on Industrial Electronics and Applications*: 1498-1502.
- [18] Chen, B., Sun, P., Liu, C., Chen, C. L., Lai, J. S., and Yu, W. (2012). High efficiency transformerless photovoltaic inverter with wide-range power factor capability. *In 2012 Twenty-Seventh Annual IEEE Applied Power Electronics Conference and Exposition (APEC)*: 575-582.
- [19] Chen, W., Hotchkiss, E., and Bazzi, A. (2018). Reconfiguration of NPC multilevel inverters to mitigate short circuit faults using back-to-back switches. *CPSS Transactions on Power Electronics and Applications* 3(1): 46-55.
- [20] Chowdhury, A. S. K., and Razzak, M. A. (2013). Single phase grid-connected photovoltaic inverter for residential application with maximum power point tracking. *In IEEE 2013 International Conference on Informatics, Electronics and Vision (ICIEV)*: 1-6.